24th IEEE International Conference on
High Performance Computing, Data, and Analytics
December 18-21, 2017 | Le Méridien Hotel | Jaipur, India

Co-sponsored by

IEEE Computer Society

HiPC Education Trust, India

In co-operation with

www.hipc.org

CALL FOR PARTICIPATION

HiPC 2017 is the 24th edition of the IEEE Conference on High Performance Computing, Data, and Analytics. It serves as a forum to present current work by top researchers in the field and to highlight the activities in Asia in the area of high performance computing and related scientific, engineering, and commercial applications. The conference technical program held on days 2, 3 and 4 will showcase three keynote speakers and three days of single track presentations of peer reviewed papers from all over the world.

Keynote Speakers

Ian Foster, Argonne National Lab and University of Chicago
Computing Just What You Need: Online Data Analysis and Reduction at Extreme Scales

Partha Ranganathan, Google
End of Moore's Law: Or, a Computer Architect's Mid-life Crisis?

Rajeev Rastogi, Amazon India
Machine Learning @ Amazon

Workshops

Complementing the main technical program, four workshops with programs and speakers that help to broaden the technical scope of the conference will be held on day 1 of the conference:

* **Foundations of Big Data Computing (BigDF)**
* **Dynamic Data Driven Smart Systems (DDDSS)**
* **Computational Fluid Dynamics (CFD)**
* **Software Composable Infrastructure (SCI)**

Special Plenary Sessions

Machine Learning and Mathematical Modeling – Towards Robust, Reliable and Certifiable Machine Learning: This panel will offer HiPC attendees expert perspectives on mathematical foundations and the potential gaps in the underlying theory that must be overcome to provide reliable, robust, and certifiable outcomes from machine learning.

HPC in India: As a special feature of HiPC 2017, a session on HPC initiatives in India is being organized and will feature presentations by leading researchers from academia, industry and Indian government R&D labs.

Student Research Symposium

The meeting will also host the 10th Student Research Symposium (SRS) aimed at fostering student research and providing a forum for students to present their work in all areas related to HPC.

Industry Research & User Symposium and Industry Exhibits

The conference encourages industry participation in the main technical program and workshops, as well as the Industry, Research and User Symposium (IRUS) and vendor exhibition. On days 2 and 3, there will be a full program of industry exhibits and events.

General Co-Chairs: Chiranjib Sur, Shell, India and Yinglong Xia, Huawei Research America, USA
Program Chair: Ümit V. Çatalyürek, Georgia Institute of Technology, USA
Steering Chair: Viktor K. Prasanna, University of Southern California, USA

Titanium Sponsor

Microsoft

Platinum Sponsors

Shell   XILINX.   SanDisk

Gold Sponsors

intel   NetApp

Exhibitors

Google   Hewlett Packard Enterprise

In association with

Digital India — Power To Empower

विज्ञान एवं प्रौद्योगिकी विभाग
DEPARTMENT OF SCIENCE & TECHNOLOGY

Ministry of Electronics and Information Technology
Government of India

सीडैक CDAC

LNMIIT

4th November, 2017